

**DEPT. OF ELECTRONICS & COMMUNICATION ENGG**

**CERTIFICATE**

**THIS IS TO CERTIFY THAT THE PROJECT ENTITLED  
SIMULATION OF MICROSTRIP PATCH ANTENNA FOR  
WIRELESS 5G COMMUNICATION SYSTEMS**

IS THE BONAFIDE WORK OF


**VALEMONI ABHINAYA (20251A0459)**

**BOTTA SREEHITHA (20251A0406)**

**MOUNICA KOMARAVALLI (20251A0448)**

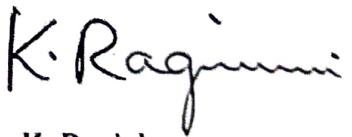
**TADIPARTHI VAISHNAVI (20251A0458)**

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE  
AWARD OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS  
AND COMMUNICATION ENGINEERING DURING THE YEAR 2023-24



GUIDE

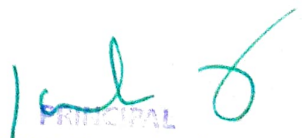
**Mr. Y. Prakash  
Asst. Professor**



**Dr. K. Ragini  
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**External Examiner**



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